

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	173	(translat\$4 near3 instruction\$2) ISA memory simulat\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L2	18	L1 SDRAM	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L3	3	L2 (segment adj table)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L4	1353	(translat\$4 near3 instruction\$2) memory simulat\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L5	40	L4 (segment adj table\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L6	31	L5 detect\$4 updat\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L7	31	L6 register\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L8	3	L4 SDRAM (segment adj table\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L9	21	L7 (base adj address\$3)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43
L10	51	L4 SDRAM	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/10/16 10:43